Homework 4

This homework is due on Saturday, September 23, 2023 at 11:59PM. Self-grades and HW Resubmissions are due the following Saturday, September 30, 2023 at 11:59PM.

1. **Adapted from Hambley P6.27**

   Suppose you have a filter with transfer function $H(\mathrm{j}\omega) = \frac{1}{1+\mathrm{j}\frac{\omega}{\omega_c}}$, with $\omega_c = 400\,\frac{\mathrm{rad}}{\mathrm{s}}$.

   The input signal of the filter with this transfer function is

   $$v_{\text{in}}(t) = 1 + 2\cos(400t + 30^\circ) + 3\cos\left(10^{10}t\right) \tag{1}$$

   Find an expression for the output voltage (you may approximate).

2. Hambley P6.33

Consider the circuit shown in Figure 1. This circuit consists of a source having an internal resistance of R_s, an RC lowpass filter, and a load resistance of R_l.

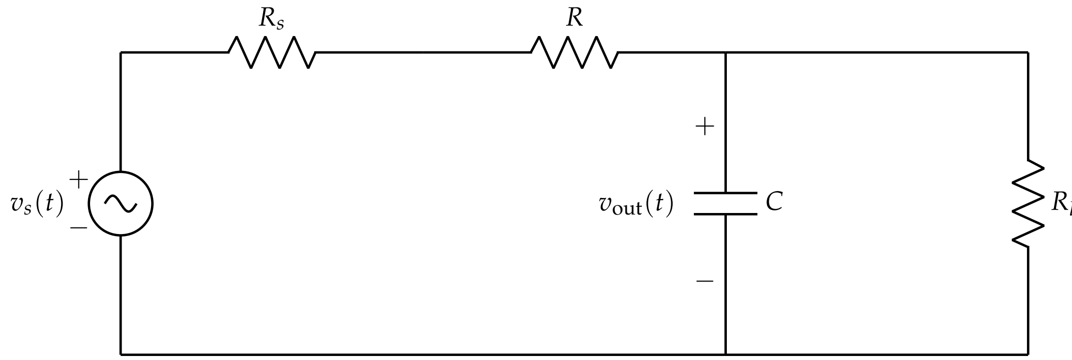


Figure 1: P6.33(a)

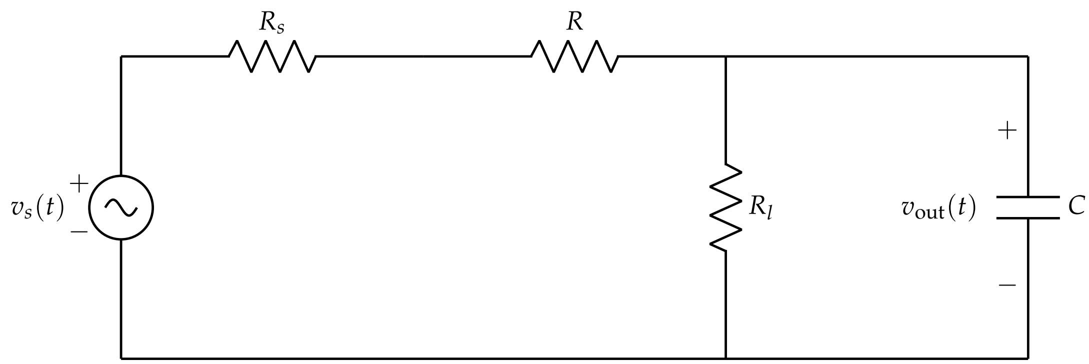


Figure 2: P6.33(b)

Show that the transfer function of this circuit is given by

$$H(\mathrm{j}\omega) = \frac{\widetilde{V}_{\text{out}}}{\widetilde{V}_s} = \frac{R_l}{R_s + R + R_l} \times \frac{1}{1 + \mathrm{j}\frac{\omega}{\omega_c}} \tag{2}$$

in which the cutoff frequency ω_c is given by $\omega_c = \frac{1}{R_t C}$ where $R_t = \frac{R_L(R_s+R)}{R_l+R_s+R}$. Notice that R_t is the parallel combination of R_l and $(R_s + R)$. *(HINT: One way to make this problem easier is to rearrange the circuit as shown in Figure 2 and then to find the Thevenin equivalent for the source and resistances.)*

3. Hambley P6.74

Derive an expression for the resonant frequency of the circuit shown in Figure 3. We define the resonant frequency to be the frequency at which the impedance is purely real (no imaginary component).

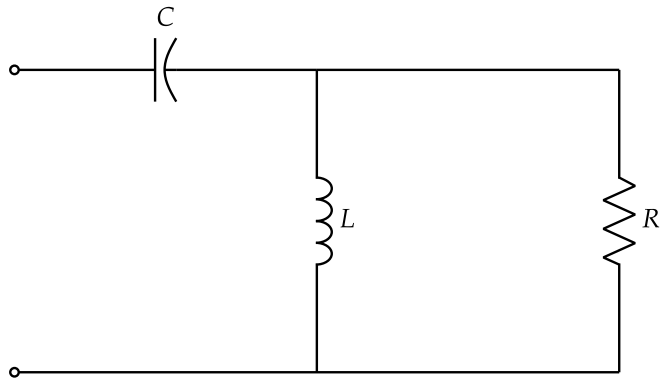


Figure 3: P6.74

4. Circuit Design

In this problem, you will find a circuit where several components have been left *blank* for you to fill in.

Assume that the op-amp is *ideal*. A special note on op amps in frequency domain analysis: The op-amps you learned about in 16A can be used in exactly the same way for setting up differential equations and even Phasor analysis in 16B. Treat them as ideal op-amps and invoke the Golden Rules.

You have at your disposal *only one of each* of the following components (not including R_1):

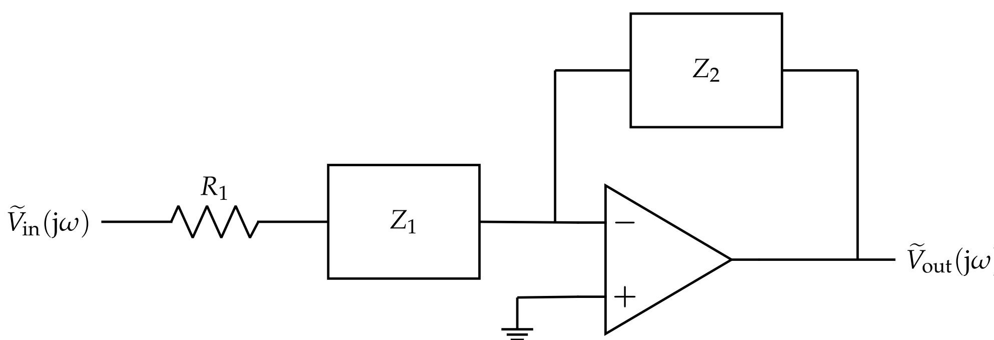
(a) an open circuit

(b) a short circuit

(c) a resistor (you choose from the values $R = 1\,\mathrm{k\Omega}, 10\,\mathrm{k\Omega}, 20\,\mathrm{k\Omega}$)

(d) a capacitor (you choose from the values $C = 0.5\,\mu\mathrm{F}, 1\,\mu\mathrm{F}, 2\,\mu\mathrm{F}$)

Consider the circuit below. The labeled voltages $\widetilde{V}_{\text{in}}(\mathrm{j}\omega)$ and $\widetilde{V}_{\text{out}}(\mathrm{j}\omega)$ are the phasor representations of $v_{\text{in}}(t)$ and $v_{\text{out}}(t)$ respectively, where $v_{\text{in}}(t)$ has the form $v_{\text{in}}(t) = v_0 \cos(\omega t + \phi)$. The transfer function $H(\mathrm{j}\omega)$ is defined as $H(\mathrm{j}\omega) = \frac{\widetilde{V}_{\text{out}}(\mathrm{j}\omega)}{\widetilde{V}_{\text{in}}(\mathrm{j}\omega)}$.



(a) Let $Z_1(\mathrm{j}\omega)$ and $Z_2(\mathrm{j}\omega)$ are the impedances of the boxes shown in the circuit diagram. **Write the expression of the transfer function $H(\mathrm{j}\omega)$.**

(b) Let R_1 be $1\,\mathrm{k\Omega}$. We have to find Z_1 and Z_2, such that the circuit's transfer function $H(\mathrm{j}\omega)$ has the following properties:

- $|H(\mathrm{j}0)| = 0$.
- $|H(\mathrm{j}\infty)| = 10$.
- $|H(\mathrm{j}10^3)| = \sqrt{50}$.

Using the fact that the circuit is a high pass filter, infer the components (we will find values later) of Z_1 and Z_2. Write the transfer function $H(\mathrm{j}\omega)$ using these components.

(HINT: Try method of elimination: figure out what Z_2 cannot be. Once you find what Z_2 is, what does Z_1 have to be for the circuit to be a filter?)

(c) **Now use the facts that $|H(\mathrm{j}\infty)| = 10$ and $R_1 = 1\,\mathrm{k\Omega}$ to find the component value of Z_2.**

(d) **Finally use the fact that $|H(\mathrm{j}10^3)| = \sqrt{50}$ and the values of R_1 and Z_2 to find the component value of Z_1.**

5. Designing Filters

In the lab, we will design various filter circuits using low-pass, high-pass, and band-pass filter elements. In this problem, we will walk through the use cases of these filter elements.

(a) First, you remember that you saw in lecture that you can build simple filters using a resistor and a capacitor. **Design a simple first-order *passive* low-pass filter with the following specification using a** $1\,\mu\text{F}$ **capacitor.** ("Passive" means that the filter does not require any power supply to operate on the input signal. Passive components include resistors, capacitors, inductors, diodes, etc., while an example of an active component would be an op-amp).

- Low-pass filter: cut-off frequency $f_c = 2400\,\text{Hz}$, $\omega_c = 2\pi \cdot 2400\,\frac{\text{rad}}{\text{s}}$. Hz can be interpreted as "cycles/sec", and $\frac{\text{rad}}{\text{s}}$ can be interpreted as "2π radians/cycle".

Recall that the cutoff-frequency of such a filter is just where the magnitude of the filter is $\frac{1}{\sqrt{2}}$ of its peak value.

Show your work to find the resistor value that creates this low-pass filter. Draw the schematic-level representation of your design. Please mark V_{in}, V_{out}, and the ground node(s) in your schematic. Round your results to two significant figures.

(b) **Now design a simple first-order *passive* high-pass filter with the following specification using a** $1\,\mu\text{F}$ **capacitor.**

- High-pass filter: cut-off frequency $f_c = 100\,\text{Hz}$, $\omega_c = 2\pi \cdot 100\,\frac{\text{rad}}{\text{s}}$

Show your work to find the resistor value that creates this high-pass filter. Draw the schematic-level representation of your design. Please mark V_{in}, V_{out}, and the ground node(s) in your schematic. Round your results to two significant figures.

(c) You can try to build a bandpass filter by cascading the first-order low-pass and high-pass filters you designed in parts (a) and (b). To do this, you might be tempted to connect the V_{out} node of your low-pass filter directly to the V_{in} node of your high-pass filter. However, if you did this, just as you saw in 16A for voltage dividers, the purported high-pass filter would "load" the low-pass filter and you might get some potentially complicated mess instead of what you wanted.

Show how you can use an ideal op-amp configured as a unity gain buffer to eliminate this loading effect to cascade the low-pass and high-pass filters, and write the resulting transfer function of the combined circuit. Draw the magnitude and phase transfer functions of the combined circuit (you can use Bode Plot approximations). What kind of filter is this?

(d) **Write down an expression for the time-domain output waveform $V_{\text{out}}(t)$ of this filter if the input voltage is $V_{\text{in}}(t) = 1\sin(1000t)\text{V}$.** Round your answer to 2 significant digits.

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