

**This homework is due on Saturday, April 29, 2023 at 11:59PM. Self-grades and HW Resubmissions are due the following Saturday, May 6, 2023 at 11:59PM.**

**1. Study Groups**

If you are a student who participated in the study group survey we gave in the early weeks of the semester, we would really appreciate your feedback on the group you were matched with. If you did not participate in a study group, we would appreciate your input on what factors went into this decision. Please fill out [this form](#) to provide any feedback. For this question, write that you completed the survey.

## 2. Practical SVD System ID

Please answer all of the questions in the Jupyter notebook associated with this homework.





#### 4. Linearizing for understanding amplification

Linearization isn't just something that is important for control, robotics, machine learning, and optimization — it is one of the standard tools used across different areas, including circuits.

The circuit below is a voltage amplifier, where the element inside the box is a bipolar junction transistor (BJT). You do not need to know what a BJT is to do this question.

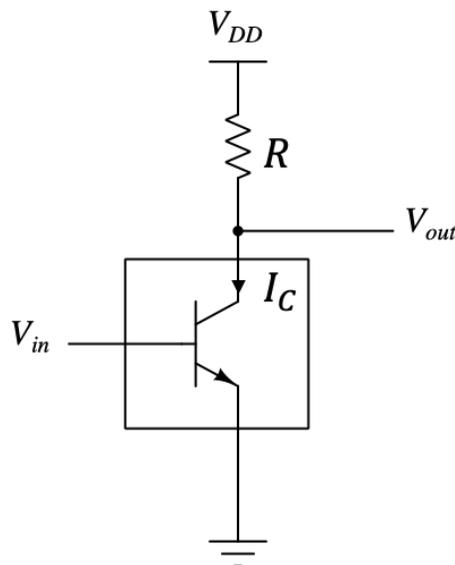


Figure 1: Voltage amplifier circuit using a BJT

The BJT in the circuit can be modeled quite accurately as a nonlinear, voltage-controlled current source, where the collector current  $I_C$  is given by:

$$I_C(V_{in}) = I_S \cdot e^{\frac{V_{in}}{V_{TH}}}, \quad (11)$$

where  $V_{TH}$  is the thermal voltage. We can assume  $V_{TH} = 26$  mV at room temperature.  $I_S$  is a constant whose exact value we are not giving you because we want you to find ways of eliminating it in favor of other quantities whenever possible.

**The goal of this circuit is to pick a particular point  $(V_{in}^*, V_{out}^*)$  so that any small variation  $\delta V_{in}$  in the input voltage  $V_{in}$  can be amplified to a relatively larger variation  $\delta V_{out}$  in the output voltage  $V_{out}$ . In other words, if  $V_{in} = V_{in}^* + \delta V_{in}$  and  $V_{out} = V_{out}^* + \delta V_{out}$ , then we want the magnitude of the 'amplification gain' given by  $\left| \frac{\delta V_{out}}{\delta V_{in}} \right|$  to be large.** We're going to investigate this amplification using linearization.

(NOTE: in this problem,  $\delta V$  is single variable indicating a small variation in  $V$ , not  $\delta \times V$ .)

- Write a symbolic expression for  $V_{out}$  as a function of  $I_C$ ,  $V_{DD}$  and  $R$  in Fig 1.
- Now let's linearize  $I_C$  in the neighborhood of an input voltage  $V_{in}^*$  and a specific  $I_C^*$ . Assume that you have found a particular pair of input voltage  $V_{in}^*$  and current  $I_C^*$  that satisfy the current equation (11).

We can look at nearby input voltages and see how much the current changes. We can write the linearized expression for the collector current around this point as:

$$I_C(V_{in}) = I_C(V_{in}^*) + g_m(V_{in} - V_{in}^*) = I_C^* + g_m \delta V_{in} \quad (12)$$

where  $\delta V_{in} = V_{in} - V_{in}^*$  is the change in input voltage, and  $g_m$  is the slope of the local linearization around  $(V_{in}^*, I_C^*)$ . **What is  $g_m$  here as a function of  $I_C^*$  and  $V_{TH}$ ?**

(HINT: Find  $g_m$  by taking the appropriate derivative around the operating point. You should recognize a part of your equation is equal to the current operating point  $I_C^* = I_C(V_{in}^*)$ , so your final form should not depend on  $I_S$ . Also, note that in circuits terminology, "operating point" is defined to be the point around which we linearize input-output relationship.)

- (c) We now have a linear relationship between small changes in current and voltage,  $\delta I_C = g_m \delta V_{in}$  around a known solution  $(V_{in}^*, I_C^*)$ .

As a reminder, the goal of this problem is to pick a particular point  $(V_{in}^*, V_{out}^*)$  so that any small variation  $\delta V_{in}$  in the input voltage  $V_{in}$  can be amplified to a relatively larger variation  $\delta V_{out}$  in the output voltage  $V_{out}$ . In other words, if  $V_{in} = V_{in}^* + \delta V_{in}$  and  $V_{out} = V_{out}^* + \delta V_{out}$ , then we want the magnitude of the "amplification gain" given by  $\left| \frac{\delta V_{out}}{\delta V_{in}} \right|$  to be large.

Plug in your linearized equation for  $I_C$  in the answer from part (a). It may help to define the output voltage operating point as  $V_{out}^*$ , where

$$V_{out}^* = V_{DD} - R I_C^* \quad (13)$$

so that we can view  $V_{out} = V_{out}^* + \delta V_{out}$  when we have  $V_{in} = V_{in}^* + \delta V_{in}$ .

**Find the linearized relationship between  $\delta V_{out}$  and  $\delta V_{in}$ .** The ratio  $\frac{\delta V_{out}}{\delta V_{in}}$  is called the "small-signal voltage gain" of this amplifier around this operating point.

- (d) Assuming that  $V_{DD} = 10\text{ V}$ ,  $R = 1\text{ k}\Omega$ , and  $I_C^* = 1\text{ mA}$  when  $V_{in}^* = 0.65\text{ V}$ , **verify that the magnitude of the small-signal voltage gain  $\left| \frac{\delta V_{out}}{\delta V_{in}} \right|$  is approximately 38.**

Next, if  $I_C^* = 9\text{ mA}$  when  $V_{in}^* = 0.7\text{ V}$  with all other parameters remaining fixed, **verify that the magnitude of the small-signal voltage gain  $\left| \frac{\delta V_{out}}{\delta V_{in}} \right|$  between the input and the output around this operating point is approximately 346.**

(HINT: Remember  $V_{TH} = 26\text{ mV}$ .)

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- (e) If you wished to make an amplifier with as large of a small signal gain as possible, **which operating (bias) point would you choose among  $V_{in}^* = 0.65\text{ V}$  and  $V_{in}^* = 0.7\text{ V}$ ?**

This shows you that by appropriately biasing (choosing an operating point), we can adjust what our gain is for small signals. While we just wanted to show you a simple application of linearization here, these ideas are developed a lot further in EE105, EE140, and other courses to create things like op-amps and other analog information-processing systems. Simple voltage amplifier circuits like these are used in everyday circuits like the sensors in your smartwatch, wireless transceivers in your phone, and communication circuits in CPUs and GPUs.

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